

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	((PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1)) same FET	USPAT	OR	OFF	2005/07/20 17:17
L2	6	((PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1)) same (range)	USPAT	OR	OFF	2005/07/20 17:17
L3	0	(FET with (turn near3 delay)) same (current adj1 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L4	44	(current adj2 regulator\$1) same (range near3 output)	USPAT	OR	OFF	2005/07/20 17:17
L5	0	(current adj2 regulator\$1) same (range near3 output)	IBM_TDB	OR	OFF	2005/07/20 17:17
L6	1	(current adj2 regulator\$1) same FET	IBM_TDB	OR	OFF	2005/07/20 17:17
L7	38	current adj2 regulator\$1	IBM_TDB	OR	OFF	2005/07/20 17:17
L8	23	713/3\$.ccls. and (current adj2 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L9	4	((improv\$3 or increas\$3) near2 range) with (current adj1 regulator)	USPAT	OR	OFF	2005/07/20 17:17
L10	4	wide near2 range near2 (current adj1 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L11	1020	advantage\$6 near5 FET	USPAT	OR	OFF	2005/07/20 17:17
L12	7	(advantage\$6 near5 FET) near5 delay	USPAT	OR	OFF	2005/07/20 17:17
L13	48	FET with (turn near3 delay)	USPAT	OR	OFF	2005/07/20 17:17
L14	0	(FET with (turn near3 delay)) with advantage\$5	USPAT	OR	OFF	2005/07/20 17:17
L15	0	(FET with (turn near3 delay)) with advantage\$6	USPAT	OR	OFF	2005/07/20 17:17
L16	2	(FET with (turn near3 delay)) same advantage\$6	USPAT	OR	OFF	2005/07/20 17:17
L17	2	((PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)) with FET	USPAT	OR	OFF	2005/07/20 17:17
L18	2	((PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)) with (FET or (field adj1 effect adj1 transistor))	USPAT	OR	OFF	2005/07/20 17:17
L19	7	((PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)) same (FET or (field adj1 effect adj1 transistor))	USPAT	OR	OFF	2005/07/20 17:17

L20	7	(operating adj1 range) with (current adj1 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L21	0	(FET or (field adj1 effect adj1 transistor)) with (turn adj1 on adj1 delay)	USPAT	OR	OFF	2005/07/20 17:17
L22	0	((transistor)) with (turn adj1 on adj1 delay)	USPAT	OR	OFF	2005/07/20 17:17
L23	1036	(switch\$3 near2 (speed or time)) with (FET or (field adj1 effect adj1 transistor))	USPAT	OR	OFF	2005/07/20 17:17
L24	86	(switch\$3 near2 (delay)) with (FET or (field adj1 effect adj1 transistor))	USPAT	OR	OFF	2005/07/20 17:17
L25	39	(switch\$3 near2 (delay)) near3 (FET or (field adj1 effect adj1 transistor))	USPAT	OR	OFF	2005/07/20 17:17
L26	409	(switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))	USPAT	OR	OFF	2005/07/20 17:17
L27	0	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current adj1 regulat\$3)	USPAT	OR	OFF	2005/07/20 17:17
L28	0	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) same (current adj1 regulat\$3)	USPAT	OR	OFF	2005/07/20 17:17
L29	1	((switch\$3 near2 (speed or time)) with (FET or (field adj1 effect adj1 transistor))) same (current adj1 regulat\$3)	USPAT	OR	OFF	2005/07/20 17:17
L30	79	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current)	USPAT	OR	OFF	2005/07/20 17:17
L31	79	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current)	USPAT	OR	OFF	2005/07/20 17:17
L32	15	((switch\$3 near2 (delay)) with (FET or (field adj1 effect adj1 transistor))) with (current)	USPAT	OR	OFF	2005/07/20 17:17
L33	35384	drive adj1 circuit	USPAT	OR	OFF	2005/07/20 17:17
L34	1	(drive adj1 circuit) same (output adj1 circuit) same (feedback adj1 amplifier) same (error amplifier)	USPAT	OR	OFF	2005/07/20 17:17
L35	11	(drive adj1 circuit) same (output adj1 circuit) same (feedback) same (error)	USPAT	OR	OFF	2005/07/20 17:17
L36	572	(drive adj1 circuit) with (FET)	USPAT	OR	OFF	2005/07/20 17:17

L37	5	((drive adj1 circuit) with (FET)) same (feedback) same (error)	USPAT	OR	OFF	2005/07/20 17:17
L38	241	(drive adj1 circuit) with (FET)	EPO; JPO; IBM_TDB	OR	OFF	2005/07/20 17:17
L39	64	(switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))	EPO; JPO	OR	OFF	2005/07/20 17:17
L40	4	((switch\$3 near2 (speed or time)) near2 (FET or (field adj1 effect adj1 transistor))) with (current)	EPO; JPO	OR	OFF	2005/07/20 17:17
L41	39	((drive adj1 circuit) with (FET)) same (feedback)	USPAT	OR	OFF	2005/07/20 17:17
L42	6	((drive adj1 circuit) with (FET near2 switch)) same (feedback)	USPAT	OR	OFF	2005/07/20 17:17
L43	133	(current adj2 regulator\$1) same FET	USPAT	OR	OFF	2005/07/20 17:17
L44	79	(PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L45	2	((current adj2 regulator\$1) same FET) same ((PWM or (pulse adj1 width adj1 modulat\$3)) near3 (current adj2 regulator\$1))	USPAT	OR	OFF	2005/07/20 17:17
L46	1	"5682287".pn.	USPAT	OR	OFF	2005/07/20 17:17
L47	150	(PWM or (pulse adj1 width adj1 modulat\$3)) near7 (current adj2 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L48	2471	(noise adj2 signal) near2 ratio	USPAT	OR	OFF	2005/07/20 17:17
L49	3	((noise adj2 signal) near2 ratio) same (current adj1 regulator\$1)	USPAT	OR	OFF	2005/07/20 17:17
L50	0	dominat\$5 near3 (inductive adj1 path)	USPAT	OR	OFF	2005/07/20 17:17
L51	0	dominat\$5 near3 (inductive near2 path\$2)	USPAT	OR	OFF	2005/07/20 17:17
L52	23	(advantage\$6 near5 FET) with drive	USPAT	OR	OFF	2005/07/20 17:17
L53	98	computer same (current adj1 regulator)	USPAT	OR	OFF	2005/07/20 17:17
L54	14657	voltage near2 conver\$6 near3 current	USPAT	OR	OFF	2005/07/20 17:17
L55	7	713/300 and (current adj1 regulator)	USPAT	OR	OFF	2005/07/20 17:17
L56	29	PLC and (current adj1 regulator)	USPAT	OR	OFF	2005/07/20 17:17
L57	13	(programmable adj1 logic adj1 controller) and (current adj1 regulator)	USPAT	OR	OFF	2005/07/20 17:17

L58	22	CPU same (current adj1 regulator)	USPAT	OR	OFF	2005/07/20 17:17
L59	5	"4553082".pn. "4858052".pn. "4634903".pn. "4636711".pn. "5534771".pn.	USPAT	OR	OFF	2005/07/20 17:17